**Условное графическое обозначение микросхемы**

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
|  | SDInCSFS SCLK | ADC |  |  |
| 15 | SDO |  |
| 18 | 14 |
| 19 |  |
| 21 |  |
|  |  |
|  |  | EOC/nINT | 13 |
| 22 | nCSTARTnPWDN |
| 23 |
|  |  |
| А\_IN0A\_IN1A\_IN2A\_IN3A\_IN4A\_IN5A\_IN6A\_IN7 |  |
| 12 |
| 11 |
| 10 |
| 9 |
| 6 |
| 5 |
| 4 |
| 3 |
|  |
| 2 | VOFFSET |
|  |
|  | EХT\_REFREF\_SEL |  |  |
| 1 | САР | 38 |
| 28 |  |  |
|  |  |  |  |
| 27 | nRST | IOUT\_R | 40 |
|  |  |
| 29 | TPIN1TPIN2TPIN3TPIN4 | CR\_ P CR\_ N |  |
| 45 |
| 32 | 46 |
| 33 |  |
| 34 |  |  |
|  |
| 16, 26, 31 | VCCDVCCADGNDAGND |  |
|  |  |  |
| 7, 37, 41, 44, 48 |  |
|  |  |
| 17, 20, 24, 25, 30 |  |
|  |
| 8, 39, 42, 43, 47 |
|  |  |

Примечание - выводы 29, 32, 33, 34 – технологические (у потребителя выводы 29, 33 подключить к VCCD, выводы 32, 34 соединить с DGND).